

### **IN THE CLAIMS**

Please cancel Claims 5 and 10-24 without prejudice or disclaimer.

Claims 1-5 (cancelled)

Claim 6 (currently amended): A digital amplifier as claimed in claim ~~5~~ 9 and including multiple clocks and wherein a first clock is used to count a second clock.

Claim 7 (original): A digital amplifier as claimed in claim 6 and wherein a divided multiple of said second clock is counted.

Claim 8 (currently amended): A digital amplifier as claimed in any of ~~claims 5 to 7~~ claim 6 and wherein output is disabled when a clocking error is detected.

Claim 9 (currently amended): A digital amplifier including a clocked modulator wherein clock activity is monitored by counting divided multiples of the clock.

~~A digital amplifier as claimed in any of claims 5 to 8 and wherein when a clocking error is detected, a parameter indicative of the error is stored.~~

Claims 10-24 (cancelled)